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Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). **KUNDUR SUBRAMANIYAN, Harish** [IN/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

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(74) Agents: **ELEVELD, Koop, J.** et al.; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

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(71) Applicant (*for all designated States except US*): **KONINKLIJKE PHILIPS ELECTRONICS N.V.** [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

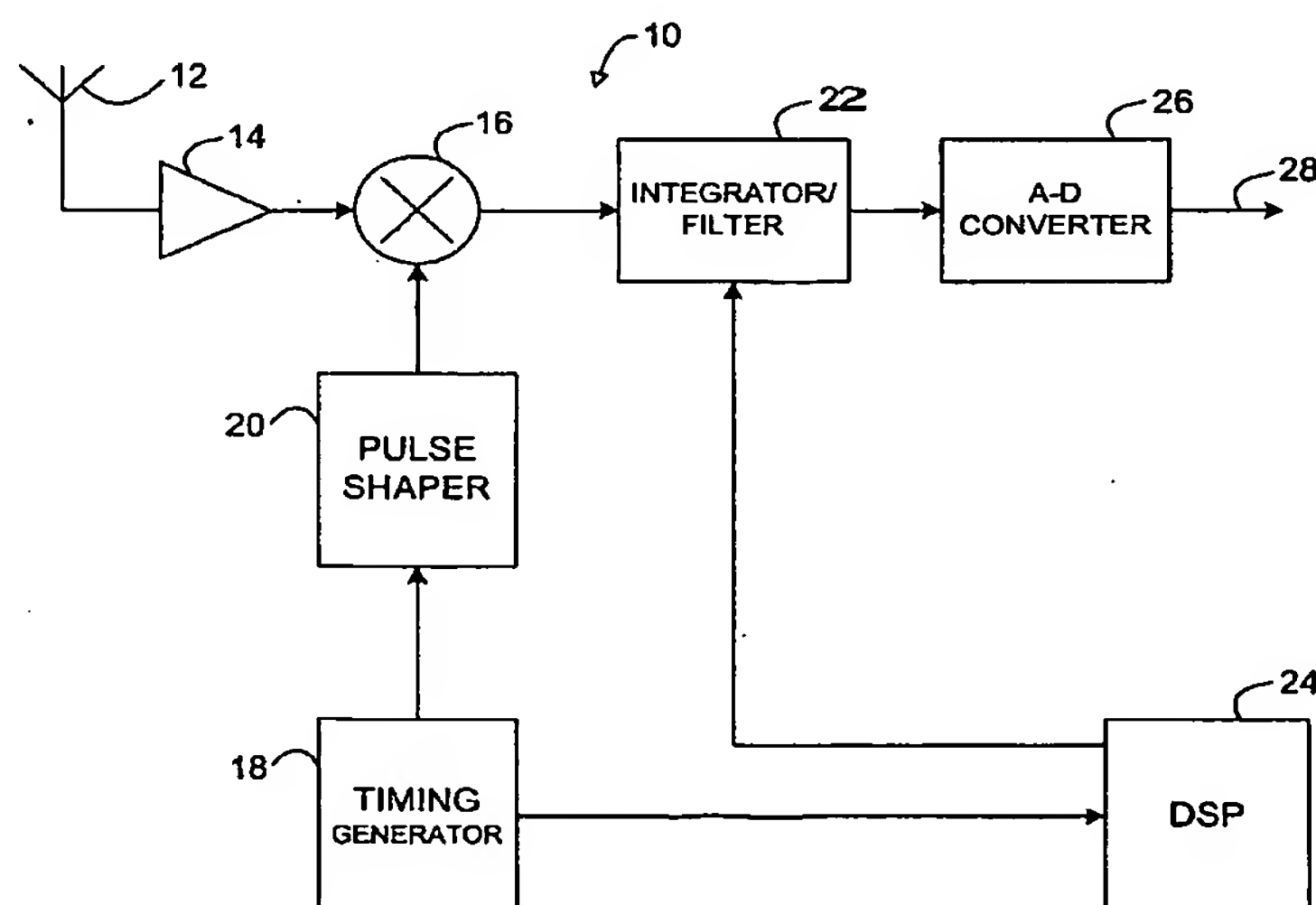
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(72) Inventors; and

(75) Inventors/Applicants (*for US only*): **VAN DER WEIDE, Gerard** [NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). **ROOVERS, Raf, L., J.** [BE/BE]; c/o

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(54) Title: RECONFIGURABLE PULSE DETECTOR IN WIRELESS COMMUNICATIONS SYSTEM



(57) Abstract: In a receiver architecture, for example for use in receiving pulses in an Ultra Wideband system, a received signal is applied to a mixer, together with pulses which correspond to expected received pulses. The mixer output is applied to a block, which can be configured either as an integrator or as a filter. When no pulses are being received, this block is configured as a filter, allowing it to lock quickly to any new sequence of pulses, while, when the system has locked to a sequence of pulses, the block is configured as an integrator, so that an improved signal-noise ratio can be achieved.

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